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PALM INTRANET**Inventor Information for 10/033513**

Inventor Name	City	State/Country
TAIPALE, DANA J.	AUSTIN	TEXAS
KOIRALA, DIPESH	AUSTIN	TEXAS

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Last Name = TAIPALE

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 7
10131662	Not Issued	061	04/24/2002	METHOD AND APPARATUS FOR DETERMINING AN UPPER DATA RATE FOR A VARIABLE DATA RATE SIGNAL	TAIPALE, DANA
10033513	Not Issued	077	12/26/2001	POST-CORRELATION INTERPOLATION FOR DELAY LOCKED LOOPS	TAIPALE, DANA J.
09499402	6477679	150	02/07/2000	METHODS FOR DECODING DATA IN DIGITAL COMMUNICATION SYSTEMS	TAIPALE, DANA J.
09498852	6477681	150	02/07/2000	METHODS FOR DECODING DATA IN DIGITAL COMMUNICATION SYSTEMS	TAIPALE, DANA J..
09131213	6310856	150	08/07/1998	CDMA COMMUNICATIONS SYSTEM HAVING A SEARCHER RECEIVER AND METHOD THEREFOR	TAIPALE, DANA JOHN
08867657	Not Issued	164	06/02/1997	SYMMETRICAL AUDIO EQUALIZER AND METHOD THEREFOR	TAIPALE, DANA

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Last Name = KOIRALA

First Name = DIPESH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 3
10131662	Not Issued	061	04/24/2002	METHOD AND APPARATUS FOR DETERMINING AN UPPER DATA RATE FOR A VARIABLE DATA RATE SIGNAL	KOIRALA, DIPESH
10033513	Not Issued	077	12/26/2001	POST-CORRELATION INTERPOLATION FOR DELAY LOCKED LOOPS	KOIRALA, DIPESH

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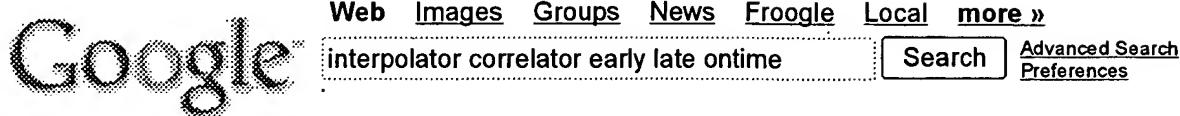
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The output of the **interpolator** is then fed directly to an **early late correlator** to generate an error signal. This signal ...

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The **early late correlator** generates a timing error ... The output of the **interpolator** is then fed directly to an **early late correlator** to generate an error ...

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On-Time, DPCCH, Early/Late, DPDCH, Control, **Correlator** pool, Beam former ...

Interpolator, Filter, DPDCH, Correlation, PN Code, Generator ...

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the classic **early late** gate tracking 'S-curve'. A ... channel **on-time** received samples is performed using a **serial correlator** started at the assumed symbol ...

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Early-late spacing of 0.25 chips, 83080 Receiver Architectures and Signal ...

Interpolator, (higher order => higher resolution), rx sign, **Correlator** ...

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complies with the well-known **early-late** gate algorithm in Figure 9, ... chip timing resolution, we introduce a linear **interpolator** into our system. ...

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Nonlinearity, Nonlinearity, +, -, Late, Early, On-Time ... **Correlator**, **Correlator**.

Reference Access Probe Generator, Reference Access Probe Generator ...

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EARLY. ON-TIME. LATE. CORRELATION TIME. FIGURE 17. CORRELATION PROCESS ...

The symbol clock is tracked by a sample **interpolator** that ...

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Page 1. 1 @ FN8019.2 CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures. 1-888 ...

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... 2). ...23 Figure 5. Basic principle of the differential **correlator** using the cyclic prefix. An asterisk ...

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Using these pulses, combination of matched filter and **interpolator** for ...

It is based on a generalized cross-**correlator** and an improved peak detector. ...

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The maximum time span prevents the **interpolator** from using data across a too ...

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STRUCTURE...37 FIGURE 22. S CURVE FOR **EARLY LATE**...

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3. The combining unit performs...

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de Gaudenzi, R.; Luise, M.;
Communications, IEEE Transactions on
Volume 39, Issue 5, May 1991 Page(s):758 - 765

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2. **Spread-spectrum ranging multipath model validation**

Braasch, M.S.; DiBenedetto, M.F.;
Aerospace and Electronic Systems, IEEE Transactions on
Volume 37, Issue 1, Jan 2001 Page(s):298 - 304

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3. **From matched filters to martingales**

Kailath, T.;
Information Theory, 1998. Proceedings. 1998 IEEE International Symposium on
16-21 Aug. 1998 Page(s):2

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4. **A combined coherent carrier recovery and decision-directed delay-lock-loop scheme for DS/SSMA communication systems employing complex spreading sequences**

Marx, F.E.; Linde, L.P.;
Spread Spectrum Techniques and Applications, 1998. Proceedings., 1998 IEEE 5th International Symposium on
Volume 3, 2-4 Sept. 1998 Page(s):837 - 842 vol.3

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5. **A non-coherent tracking scheme for the RAKE receiver that can cope with unresolvable multipath**

Aue, V.; Fettweis, G.P.;
Communications, 1999. ICC '99. 1999 IEEE International Conference on
Volume 3, 6-10 June 1999 Page(s):1917 - 1921 vol.3

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6. **Comparison of PN code tracking digital DLL's for direct sequence spread spectrum system**

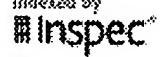
Maljevic, I.; Sousa, E.S.;
Personal, Indoor and Mobile Radio Communications, 2004. PIMRC 2004. 15th IEEE International Symposium on
Volume 4, 5-8 Sept. 2004 Page(s):2944 - 2948 Vol.4

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L47	221	correlator and (early with late) and interpolat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L48	88	L32 and L47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L49	45	L47 and L43	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L50	26	L48 and L43	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52

L51	431	375/136	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L52	4	L48 and L51	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L53	901	375/142	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L54	18	L48 and L53	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L55	1353	375/147	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L56	19	L48 and L55	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L57	1450	375/343	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L58	11	L48 and L57	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52

L59	1420	375/373	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L60	1	L48 and L59	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L61	3350	375/376	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52
L62	1	L48 and L61	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/09 10:52